Rapidio The Embedded System Interconnect

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"RapidIO is the interconnect of choice for next generation wireless ... Tundra's smart technology connects critical components in high performance embedded systems around the world. For more ...

~~Tundra Semiconductor Announces First End-to-End RapidIO Baseband Development Platform~~

RapidIO products also assist the high-performance embedded industry by providing ... to application software. This interconnect is designed typically for common .25 and .18 micron configuration memory ...

~~RapidIO Products Information~~

Mercury is co-developer of the RapidIO interconnect architecture ... is the leading provider of high-performance embedded, real-time digital signal and image processing computer systems. Mercury's ...

~~Mercury Computer Systems, Inc. Announces Availability of its Serial RapidIO Based Silicon IP Core~~

Let us follow the evolution of system interconnect protocols by observing the features ... standard for the design of communication architectures for highperformance embedded microcontrollers. AMBA ...

RapidIO - The Embedded System Interconnect brings together one essential volume on RapidIO interconnect technology, providing a major reference work for the evaluation and understanding of RapidIO. Covering essential aspects of the specification, it also answers most usage questions from both hardware and software engineers. It will also serve as a companion text to the specifications when developing or working with the RapidIO interconnect technology. Including the history of RapidIO and case of studies of RapidIO deployment, this really is the definitive reference guide for this new area of technology.

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This book describes the most frequently used high-speed serial buses in embedded systems, especially those used by FPGAs. These buses employ SerDes, JESD204, SRIO, PCIE, Aurora and SATA protocols for chip-to-chip and board-to-board communication, and CPCIE, VPX, FC and Infiniband protocols for inter-chassis communication. For each type, the book provides the bus history and version info, while also assessing its advantages and limitations. Furthermore, it offers a detailed guide to implementing these buses in FPGA design, from the physical layer and link synchronization to the frame format and application command. Given its scope, the book offers a valuable resource for researchers, R&D engineers and graduate students in computer science or electronics who wish to learn the protocol principles, structures and applications of high-speed serial buses.

In the second phase of this work, we propose several novel fault-tolerant architectures for RapidIO-based space systems and quantitatively evaluate these architectures through a unique combination of analytical metrics and simulation studies. The results from this phase show several promising architectures in terms of achieving a balance between performance, fault-tolerance, size, power, and cost.

The 4th International Conference on Electronic, Communications and Networks (CECNet2014) inherits the fruitfulness of the past three conferences and lays a foundation for the forthcoming next year in Shanghai. CECNet2014 was hosted by Hubei University of Science and Technology, China, with the main objective of providing a comprehensive global foru

These proceedings of the 2014 Pacific-Asia Workshop on Computational Intelligence in Industrial Application (CIIA 2014) include 81 peer-reviewed papers. The topics covered in the book include: (1) Computer Intelligence, (2) Application of Computer Science and Communication, (3) Industrial Engineering, Product Design and Manufacturing, (4) Automatio

This unique book provides you with practical guidance on understanding and interpreting signal integrity (SI) performance to help you with your challenging circuit board design projects. You find high-level discussions of important SI concepts presented in a clear and easily accessible format, including question and answer sections and bulleted lists.This valuable resource features rules of thumb and simple equations to help you make estimates of critical signal integrity parameters without using circuit simulators of CAD (computer-aided design). The book is supported with over 120 illustrations, nearly 100 equations, and detailed reference lists at the end of each chapter.

This volume brings together contributions representing the state-of-the-art in new multimedia and future technology information research, currently a major topic in computer science and electronic engineering. Researchers aim to interoperate multimedia frameworks, transforming the way people work and interact with multimedia data. This book covers future information technology topics including digital and multimedia convergence, ubiquitous and pervasive computing, intelligent computing and applications, embedded systems, mobile and wireless communications, bio-inspired computing, grid and cloud computing, semantic web, human-centric computing and social networks, adaptive and context-aware computing, security and trust computing and related areas. Representing the combined proceedings of the 9th International Conference on Multimedia and Ubiquitous Engineering (MUE-15) and the 10th International Conference on Future Information Technology (Future Tech 2015), this book aims to provide a complete coverage of the areas outlined and to bring together researchers from academic and industry and other practitioners to share their research ideas, challenges and solutions.

Field programmable gate arrays (FPGAs) are an increasingly popular technology for implementing digital signal processing (DSP) systems. By allowing designers to create circuit architectures developed for the specific applications, high levels of performance can be achieved for many DSP applications providing considerable improvements over conventional microprocessor and dedicated DSP processor solutions. The book addresses the key issue in this process specifically, the methods and tools needed for the design, optimization and implementation of DSP systems in programmable FPGA hardware. It presents a review of the leading-edge techniques in this field, analyzing advanced DSP-based design flows for both signal flow graph- (SFG-) based and dataflow-based implementation, system on chip (SoC) aspects, and future trends and challenges for FPGAs. The automation of the techniques for component architectural synthesis, computational models, and the reduction of energy consumption to help improve FPGA performance, are given in detail. Written from a system level design perspective and with a DSP focus, the authors present many practical application examples of complex DSP implementation, involving: high-performance computing e.g. matrix operations such as matrix multiplication; high-speed filtering including finite impulse response (FIR) filters and wave digital filters (WDFs); adaptive filtering e.g. recursive least squares (RLS) filtering; transforms such as the fast Fourier transform (FFT). FPGA-based Implementation of Signal Processing Systems is an important reference for practising engineers and researchers working on the design and development of DSP systems for radio, telecommunication, information, audio-visual and security applications. Senior level electrical and computer engineering graduates taking courses in signal processing or digital signal processing shall also find this volume of interest.